

ISO/IEC JTC 1/SC 17 N **1116**

Date: 2001-10-08

ISO/IEC CD 7811-7

ISO/IEC JTC 1/SC 17/WG 1

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**Identification cards — Recording technique — Part 7: Magnetic stripe —
High coercivity high density**



WG1 previous versions N969, N1065, N1093

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Document type: International Standard
Document subtype: Not applicable
Document stage: (30) Committee
Document language: E

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Foreword

ISO (the International Organization for Standardization) and IEC (the International Electrotechnical Commission) form the specialized system for world-wide standardization. National bodies that are members of ISO or IEC participate in the development of International Standards through technical committees established by the respective organization to deal with particular fields of technical activity. ISO and IEC technical committees collaborate in fields of mutual interest. Other international organizations, governmental and non-governmental, in liaison with ISO and IEC, also take part in the work.

International Standards are drafted in accordance with the rules given in the ISO/IEC Directives, Part 3.

In the field of information technology, ISO and IEC have established a joint technical committee, ISO/IEC JTC 1. Draft International Standards adopted by the joint technical committee are circulated to national bodies for voting. Publication as an International Standard requires approval by at least 75 % of the national bodies casting a vote.

ISO/IEC 7811 consists of the following parts, under the general title *Identification cards – Recording technique*:

- Part 1: *Embossing*
- Part 2: *Magnetic stripe-Low coercivity*
- Part 6: *Magnetic stripe-High coercivity*
- Part 7: *Magnetic stripe-High coercivity high density*

This edition is new and was prepared by JTC1/SC17/WG1 *Physical characteristics and test methods for ID cards*. Portions of this standard are identical to ISO/IEC 7811-2 and ISO/IEC 7811-6, however the user is encouraged to review the entire standard. The major differences between this standard and ISO/IEC 7811-2 and ISO/IEC 7811-6 are listed below.

1. The bit density has increased from 8,27 bits/mm to 40 bits/mm.
2. The encoding technique referred to as MFM is used in place of F2F. This change doubles the data storage density for the same minimum transition spacing with only a small reduction in the self-clocking ability.
3. The 3 tracks have been replaced by 6 tracks that are approximately half the width so that they occupy the same space on the card. These are located so that readers designed to read the high density tracks will also be able to read cards conforming to ISO/IEC 7811-2 and ISO/IEC 7811-6.
4. Data is distributed in frames with synchronisation characters to aid in error recovery, and there is a CRC for each frame and a track CRC. Data recorded on each track is independent from other tracks (error detection and correction for each track is on the same track), even though it may be only part of the message on the card.
5. Error detection and correction is included using a shortened Reed-Solomon code. The amount of error correction is fixed for all card sizes.
6. The magnetic stripe area extends completely to the left and right edge of the card.
7. In Table 1, test density values have changed, the resolution requirement has changed from 0,7 to 0,8, the test for Waveform has been deleted, and Overwrite has been added to the requirements.
8. The maximum coercivity in Table D.1 of informative Annex D has been changed from 335 kA/m (4200 Oe) to 250kA/m (3125 Oe).

Notes in this standard are only used for giving additional information intended to assist in the understanding or use of the standard and do not contain provisions or requirements to which it is necessary to conform in order to be able to claim compliance with this standard.

Annex A, C, D and E are informative and Annex B is normative for this part of ISO/IEC 7811.

Identification cards — Recording technique — Part 7: Magnetic stripe — High coercivity high density

1 Scope

This part of ISO/IEC 7811 is one of a series of standards describing the characteristics for identification cards as defined in the definitions clause and the use of such cards for international interchange.

This part of ISO/IEC 7811 specifies requirements for a high coercivity magnetic stripe (including any protective overlay) on an identification card, the encoding technique and coded character sets. It takes into consideration both human and machine aspects and states minimum requirements.

Coercivity influences many of the quantities specified in this part of ISO/IEC 7811 but is not itself specified. The main characteristic of the high coercivity magnetic stripe is its improved resistance to erasure. This is achieved with minimal probability of damage to other magnetic stripes by contact while retaining read compatibility with magnetic stripes as defined in ISO/IEC 7811-2.

This standard provides for a card capacity of approximately 10 times that of a card conforming to ISO/IEC 7811-6. The number of tracks has been increased to 6, each track being approximately half the width of tracks conforming to ISO/IEC 7811-6, located so that readers designed to read these high density tracks will also be able to read cards conforming to ISO/IEC 7811-2 and ISO/IEC 7811-6. Data is encoded in 8 bit bytes using the MFM encoding technique. Data framing is used to limit error propagation and error correction techniques further improve reliability of reading.

It is the purpose of this series of standards to provide criteria to which cards shall perform. No consideration is given within these standards to the amount of use, if any, experienced by the card prior to test. Failure to conform to specified criteria should be negotiated between the involved parties.

ISO/IEC 10373-2 specifies the test procedures used to check cards against the parameters specified in this part of ISO/IEC 7811.

NOTE Numeric values in the SI and/or Imperial measurement system in this part of ISO/IEC 7811 may have been rounded off and therefore are consistent with, but not exactly equal to, each other. Either system may be used, but the two should not be intermixed or reconverted. The original design was made using the Imperial measurement system.

2 Conformance

A prerequisite for conformance with this part of ISO/IEC 7811 is conformance with ISO/IEC 7810. An identification card is in conformance with this part of ISO/IEC 7811 if it meets all mandatory requirements specified herein. Default values apply if no others are specified.

3 Normative references

The following normative documents contain provisions which, through reference in this text, constitute provisions of this part of ISO/IEC 7811. For dated references, subsequent amendments to, or revisions of, any of these publications do not apply. However, parties to agreements based on this part of ISO/IEC 7811 are encouraged to investigate the possibility of applying the most recent editions of the normative documents indicated below. For undated references, the latest edition of the normative document referred to applies. Members of ISO and IEC maintain registers of currently valid International Standards.

ISO/IEC 7811-7: 2001(E)

ISO/IEC 4287-1, *Surface roughness - Terminology - Part 1: Surface and its parameters.*

ISO/IEC 7810, *Identification cards - Physical characteristics.*

ISO/IEC 10373-1, *Test methods – Part 1: General characteristics tests.*

ISO/IEC 10373-2, *Test methods – Part 2: Cards with magnetic stripe.*

4 Terms and definitions

For the purposes of this part of ISO/IEC 7811, the terms and definitions given in ISO/IEC 7810 and the following apply.

4.1 primary standard

set of reference cards established and maintained by Physikalisch-Technische Bundesanstalt (PTB) that represent the values of U_R and I_R designated RM7811-7

4.2 secondary standard

reference card designated RM7811-7 that is related to the primary standard as stated in the calibration certificate supplied with each card

NOTE Secondary standards can be ordered from Physikalisch-Technische Bundesanstalt (PTB), FLab. 2.24 - Bundesallee 100, D-38116 Braunschweig, Germany. The source of secondary standards will be maintained at least until 2005.

4.3 unused un-encoded card

card possessing all the components required for its intended purpose, which has not been subjected to any personalization or testing operation, and which has been stored in a clean environment with no more than 48 hour exposure to day-light at temperatures between 5 °C to 30 °C and humidity between 10% to 90% without experiencing thermal shock

4.4 unused encoded card

card according to 4.3 that has only been encoded with all the data required for its intended purpose (e.g. magnetic encoding, embossing, electronic encoding)

4.5 returned card

card according to 4.4 after it has been issued to the card holder and returned for the purpose of testing

4.6 flux transition

location of the greatest rate of change with distance of the magnetisation

4.7 reference current

I_R
minimum recorded current amplitude under the given test conditions that causes, on the reference card, a readback signal amplitude equal to 80% of the reference signal amplitude U_R , at a density of 40 flux transitions per millimetre (1016 flux transitions per inch) as shown in Figure 6

4.8 reference flux level

F_R
flux level in the test head that corresponds to the reference current I_R

4.9**test recording currents**

two recording currents defined by:

I_{\min} = Recording current corresponding to 2,8 F_R

I_{\max} = Recording current corresponding to 3,5 F_R

\$Note It may be necessary to reduce I_{\min} and I_{\max} to improve signal amplitude performance which would also reduce the upper range of coercivity that can be used. Consider $I_{\min} = 2$ and $I_{\max} = 2.5$. New values will be the result of the PTB certification work

4.10**individual signal amplitude**

U_i

base-to-peak amplitude of a single readback voltage signal

4.11**average signal amplitude**

U_A

sum of the absolute value of the amplitude of each signal peak (U_i) divided by the number of signal peaks (n) for a given track over the length of the magnetic stripe area

4.12**reference signal amplitude**

U_R

maximum value of the average signal amplitude of a reference card corrected to the primary standard

4.13**physical recording density**

number of flux transitions per unit length recorded on a track

4.14**bit density**

number of data bits stored per unit of length (bits/mm or bpi)

4.15**bit cell**

distance for a data bit nominally the reciprocal of the bit density. See Figure 8

4.16**average bit cell**

B_a

product of bit cell length and sum of the actual distances for all flux transition intervals on a track divided by the sum of the nominal distances for all flux transition intervals on the track

4.17**local average bit cell**

B_{a6}

comparison reference for a given flux transition interval equal to the sum of the actual distances for the previous 6 flux transition intervals divided by the sum of the nominal distances for the previous 6 flux transition intervals

**4.18****demagnetisation current**

I_d

D C current value that reduces the average signal amplitude to 80% of the reference signal amplitude (U_R) on a secondary reference card that has been encoded at a density of 40 ft/mm (1016 ftpi) at a current of I_{\min}

4.19

L_1

short distance between adjacent flux transitions nominally equal to 1 times the bit cell

ISO/IEC 7811-7: 2001(E)

4.20

L_2

medium distance between adjacent flux transitions nominally equal to 1,5 times the bit cell

4.21

L_3

long distance between adjacent flux transitions nominally equal to 2 times the bit cell

4.22

FSC

frame synchronisation character

4.23

CRC

cyclic redundancy check

4.24

CP

column parity

4.25

U_F

magnitude of the individual element at specified frequency of the Fourier spectrum for a given track over the length of the magnetic stripe area

5 Physical characteristics of the identification card

The identification card shall conform to the specification given in ISO/IEC 7810.

WARNING --The attention of card issuers is drawn to the fact that information held on the magnetic stripe may be rendered ineffective through contamination by contact with dirt and certain commonly used chemicals including plasticizers. It should also be noted that any printing or screening placed on top of the magnetic stripe must not impair the function of the magnetic stripe.

5.1 Magnetic stripe area warpage

Application of a 2,2 N (0.5 lbf) load evenly distributed on the front face opposite the magnetic stripe shall bring the entire stripe within 0,08 mm (0.003 in) of the rigid plate.

5.2 Surface distortions

There shall be no surface distortions, irregularities or raised areas on both the front and the back of the card in the area shown in Figure 1 that might interfere with the contact between the magnetic head and magnetic stripe.

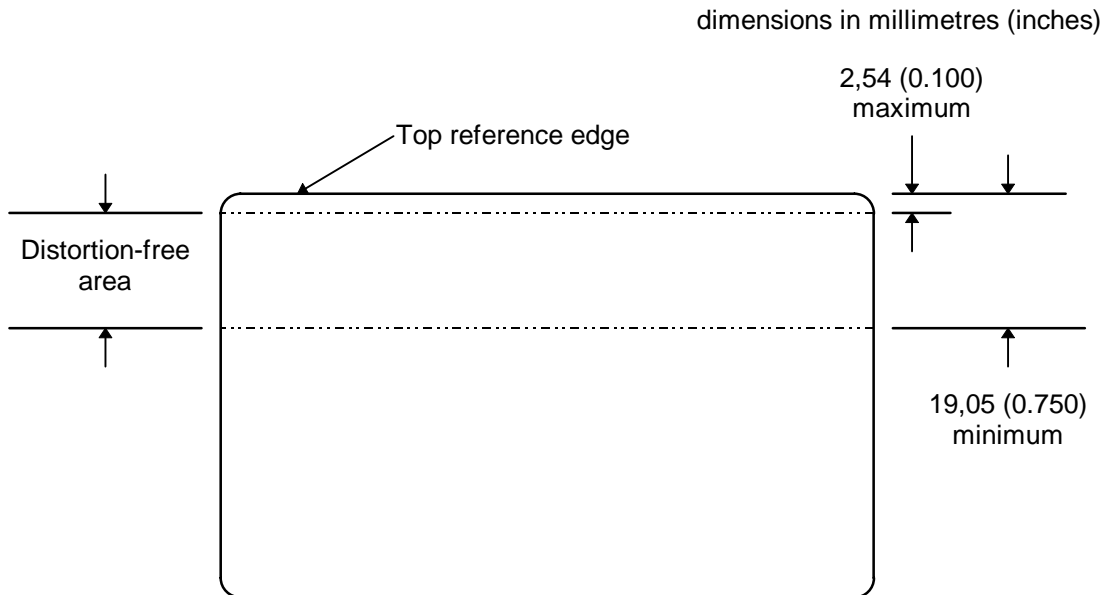


Figure 1 — Distortion-free area on card with magnetic stripe

If a raised signature panel area is located on the front or back of the card, then it shall be no closer to the top edge of the card than 19,05 mm (0.750 in).

NOTE Raised areas and distortions on other areas of the card may cause card transport problems with magnetic stripe processing equipment resulting in reading or writing errors.

6 Physical characteristics of the magnetic stripe

6.1 Height and surface profile of the magnetic stripe area

The magnetic stripe area is located on the back of the card as shown in Figure 2.

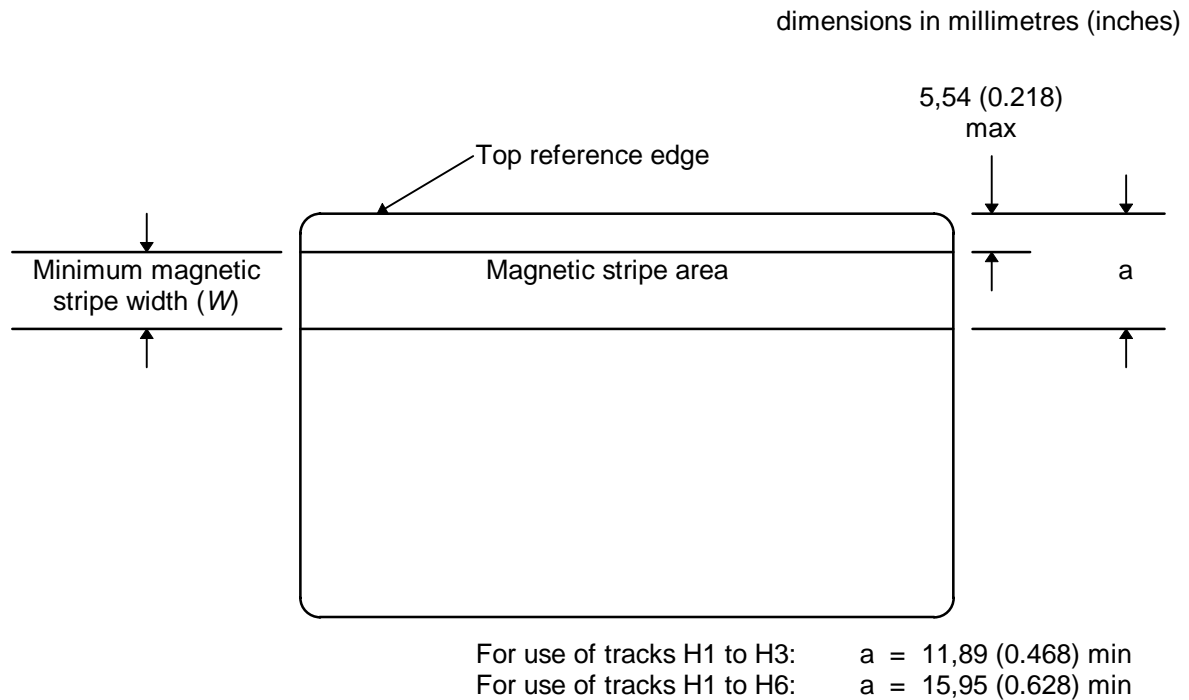


Figure 2 — Location of magnetic material

6.1.1 Surface profile of the magnetic stripe area

The maximum vertical deviation (a) of the transverse surface profile of the magnetic stripe area is shown below. See Figures 3, 4, and 5. The slope of the surface profile curve shall be limited to: $-4a/W < \text{slope} < 4a/W$

When the bending stiffness value (see ISO/IEC 7810) for the card is 20 mm or more then the surface profile limits are:

Minimum stripe width	As shown in Figure 3A	As shown in Figure 3B
$W = 6,35 \text{ mm (0.25 in)}$	$a \leq 4,8 \mu\text{m (189 } \mu\text{in)}$	$a \leq 2,9 \mu\text{m (114 } \mu\text{in)}$
$W = 10,28 \text{ mm (0.405 in)}$	$a \leq 7,7 \mu\text{m (303 } \mu\text{in)}$	$a \leq 4,6 \mu\text{m (181 } \mu\text{in)}$

When the bending stiffness value (see ISO/IEC 7810) for the card is less than 20 mm then the surface profile limits are:

Minimum stripe width	As shown in Figure 3A	As shown in Figure 3B
$W = 6,35 \text{ mm (0.25 in)}$	$a \leq 3,7 \mu\text{m (146 } \mu\text{in)}$	$a \leq 2,3 \mu\text{m (91 } \mu\text{in)}$
$W = 10,28 \text{ mm (0.405 in)}$	$a \leq 5,9 \mu\text{m (232 } \mu\text{in)}$	$a \leq 3,7 \mu\text{m (146 } \mu\text{in)}$

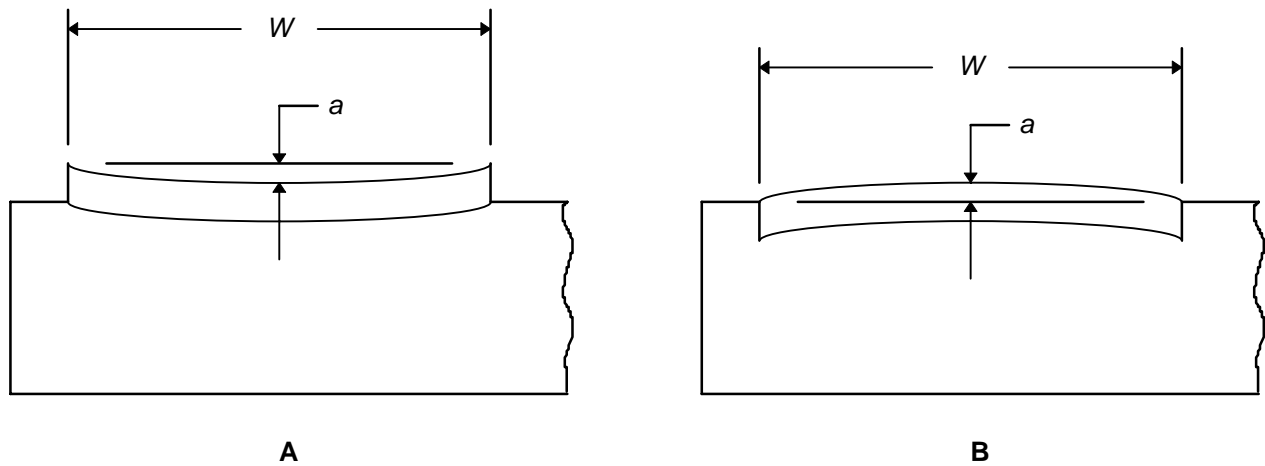


Figure 3 — Surface profile

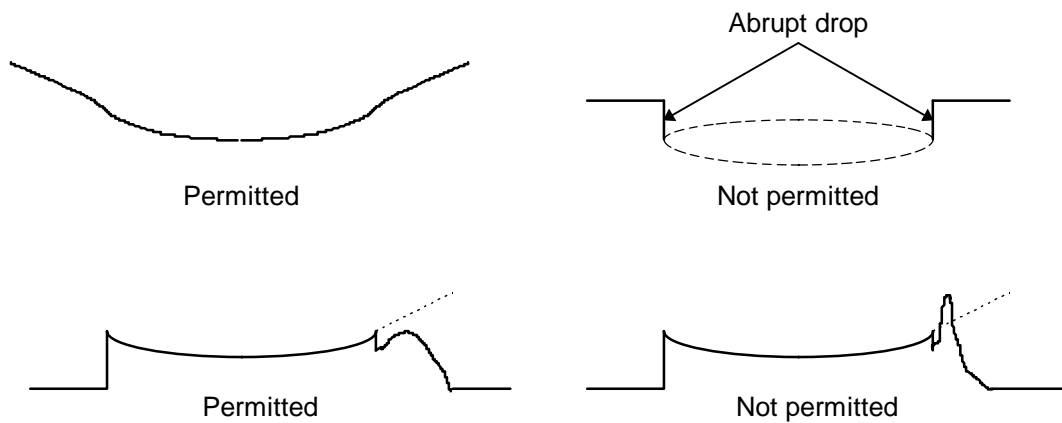


Figure 4 — Surface profile examples



Irregular profiles as shown may result in poor quality encoding.

Figure 5 — Irregular surface profile examples

6.1.2 Height of the magnetic stripe area

The vertical deviation (h) of the magnetic stripe area relative to the adjacent surface of the card shall be:

$$-0,005 \text{ mm } (- 200 \text{ } \mu\text{in}) \leq h \leq 0,038 \text{ mm } (1500 \text{ } \mu\text{in})$$

Spiking in the profile caused by the material "squirt out" in hot stamping is not part of the stripe. It shall not extend above the magnetic stripe area height (h) as defined above.

6.2 Surface roughness

The average surface roughness (R_a) of the magnetic stripe area shall not exceed 0,40 μm (15.9 μin) in both the longitudinal and transverse directions. Refer to ISO/IEC 4287 Part 1.

6.3 Adhesion of stripe to card

The stripe shall not separate from the card under normal use.

6.4 Wear of magnetic stripe from read/write head

Average signal amplitude (U_A) and individual signal amplitude (U_i) are measured before and after 2000 wear cycles and shall result in:

$$U_{A \text{ after}} \geq 0,60 U_{A \text{ before}} \quad \text{and} \quad U_{i \text{ after}} \geq 0,80 U_{A \text{ after}}$$

\$Are there any results from French proposal for a different wear test?

6.5 Resistance to chemicals

Average signal amplitude (U_A) and individual signal amplitude (U_i) are measured before and after short term exposure (as defined in the referenced Test Method document) shall result in:

$$U_{A \text{ after}} \geq 0,90 U_{A \text{ before}} \quad \text{and} \quad U_{i \text{ after}} \geq 0,90 U_{A \text{ after}}$$

Average signal amplitude (U_A) and individual signal amplitude (U_i) are measured before and after long term exposure (24 hours) to acid and alkaline artificial perspiration, as defined in the referenced Test Method document.

$$U_{A \text{ after}} \geq 0,90 U_{A \text{ before}} \quad \text{and} \quad U_{i \text{ after}} \geq 0,90 U_{A \text{ after}}$$

7 Performance characteristics for the magnetic material

The purpose of this section is to enable magnetic interchangeability between card and processing systems. Media coercivity is not specified. The media's performance criteria, regardless of coercivity, is specified in clause 7.3.

7.1 General

This method uses a reference card whose material is traceable to the primary standard (see clause 4). All signal amplitude results from the use of the secondary reference card must be corrected by the factor supplied with the secondary reference card.

7.2 Testing and operating environment

The testing environment for signal amplitude measurements is 23 $^{\circ}\text{C} \pm 3^{\circ}\text{C}$ (73 $^{\circ}\text{F} \pm 5^{\circ}\text{F}$) and 40% to 60% relative humidity. When tested under otherwise identical conditions, the average signal amplitude measured at 40 ft/mm (1016 fpi) shall not deviate from its value in the above test environment by more than 15% after 5 minute exposure over the following operating environment range:

temperature	-35 $^{\circ}\text{C}$ to 50 $^{\circ}\text{C}$ (-31 $^{\circ}\text{F}$ to 122 $^{\circ}\text{F}$)
relative humidity	5% to 95%

7.3 Signal amplitude requirements for magnetic media

The requirements for recording characteristics of the card are shown in Table 1, and Figures 6 and 7. The media's performance requirements specified in section 7.3 shall be met in order to achieve improved resistance to erasure, and to enable magnetic interchange between card and processing systems. The properties in Annex D are

intended as guidelines for magnetic material. Annex D is informative and shall not be used as performance criteria for cards.

Table 1 — Signal amplitude requirements for unused unencoded cards

Description	Density ft/mm (ftpi)	Test recording current	Signal amplitude result	Requirement
Signal amplitude	20 (508)	I_{min}	U_{A1}	$0,8 U_R \leq U_{A1} \leq 1,2 U_R$
Signal amplitude	20 (508)	I_{min}	U_{i1}	$U_{i1} \leq 1,26 U_R$
Signal amplitude	20 (508)	I_{max}	U_{A2}	$U_{A2} \geq 0,8 U_R$
Signal amplitude	40 (1016)	I_{max}	U_{i2}	$U_{i2} \geq 0,65 U_R$
Resolution	40 (1016)	I_{max}	U_{A3}	$U_{A3} \geq 0,8 U_{A2}$
Erasure	0	I_{min} , DC	U_{A4}	$U_{A4} \leq 0,03 U_R$
Extra pulse	0	I_{min} , DC	U_{i4}	$U_{i4} \leq 0,05 U_R$
Demagnetisation	0	I_d , DC	U_{A5}	$U_{A5} \geq 0,64 U_R$
Demagnetisation	0	I_d , DC	U_{i5}	$U_{i5} \geq 0,54 U_R$
Overwrite	20 (508) 40 (1016)	I_{max} I_{min}	U_{F20} U_{F40}	$U_{F40} \leq 0,03 U_{F20}$
The slope of the saturation curve shall never be positive between I_{min} and I_{max}				
It is not permissible to combine the above requirements mathematically.				

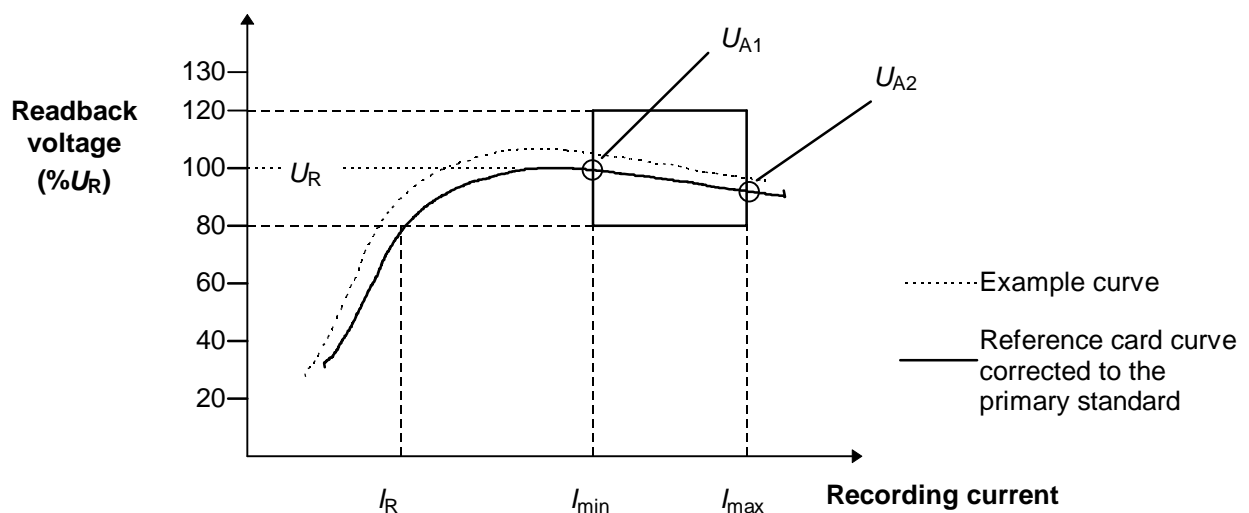


Figure 6 — Saturation curve example showing tolerance area

NOTE The curve defines the primary standard response (on a card). The window parameters define a card that will be functional in the machine readable environment.

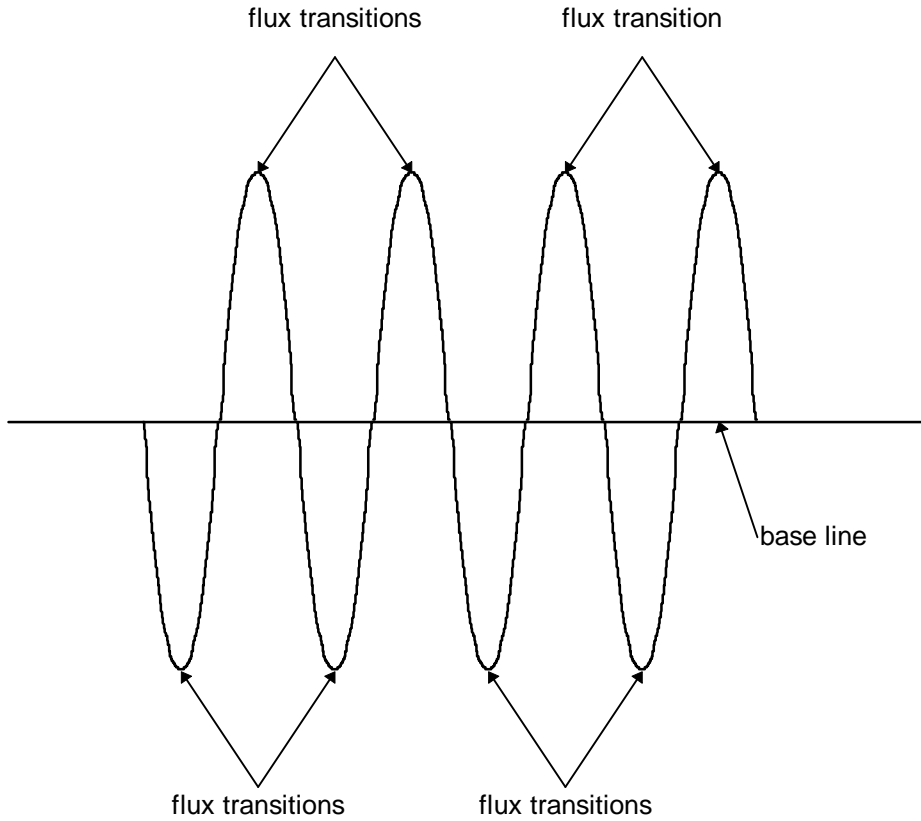


Figure 7 — Waveform example

8 Encoding technique



The encoding technique for each track is known as two-frequency recording. This method allows for serial recording of self-clocking data. The encoding comprises data and clocking transitions together. A flux transition occurring between clocks signifies that the bit is a "one" and the absence of a flux transition between clocking transitions signifies that the bit is a "zero" (see Figure 8).

The encoding technique for each track shall be Modified Frequency Modulation (MFM) recording for which the conditions are:

- a flux transition shall be written at the centre of each bit cell containing a ONE,
- a flux transition shall be written at each cell boundary between adjacent bit cells containing ZEROS.

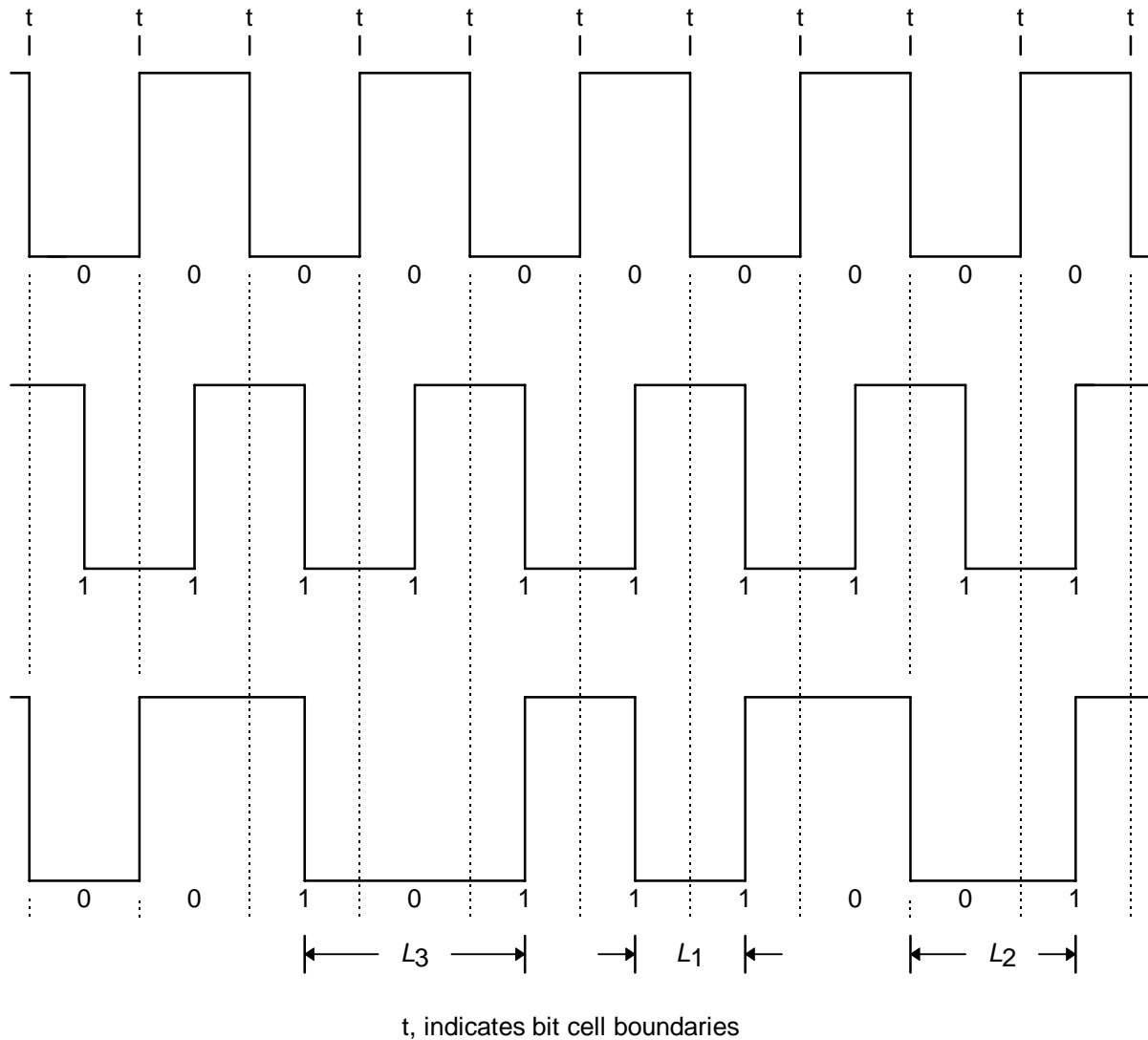


Figure 8 — Examples of MFM encoding

The data shall be recorded as a synchronous sequence of characters without intervening gaps.

NOTE 1 Recording with a write current which is less than I_{min} may result in poor quality encoding.

NOTE 2 MFM is the same as the FM technique described in ISO/IEC 7811-6 except that clocking flux transitions for 1 bits have been removed. This results in a loss of some of the self-clocking feature with FM encoding and requires more accuracy for flux transition intervals. With this technique there may not be a flux transition at the bit cell boundary.

9 Encoding specification

9.1 Angle of recording

The angle of recording shall be normal to the nearest edge of the card parallel to the magnetic stripe with a tolerance of ± 20 minutes. The angle of recording (α) is determined by measuring the angle of the head gap when the reading amplitude is maximum (see Figure 9).

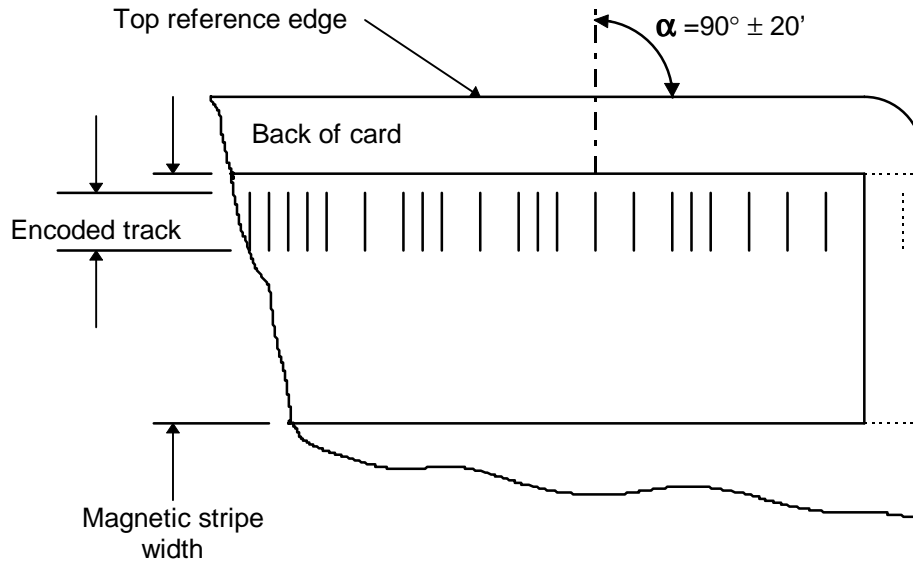


Figure 9 — Angle of recording


9.2 Nominal bit density

The nominal bit density for each of the tracks shall be 40 bits/mm (1016 bpi).

9.3 Flux transition spacing variation

Flux transition spacing variations for all tracks are given in Table 2.

Table 2 — Flux transition spacing variation

Term	Description	Requirement	 Variation
B_a	Average bit cell	$23 \mu\text{m} (91 \mu\text{in}) \leq B_a \leq 25,3 \mu\text{m} (100 \mu\text{in})$	-8% to +1%
B_{a6}	Local average bit cell	$0,92 B_a \leq B_{a6} \leq 1,08 B_a$	$\pm 8\%$ of B_a
L_1	Short interval	$0,80 B_{a6} \leq L_1 \leq 1,20 B_{a6}$	-20% to +20% of L_1
L_2	Medium interval	$1,30 B_{a6} \leq L_2 \leq 1,65 B_{a6}$	-13.3% to +10% of L_2
L_3	Long interval	$1,85 B_{a6} \leq L_3 \leq 2,25 B_{a6}$	-7,5% to +12,5% of L_3

9.4 Signal amplitude requirements

The requirements for signal amplitude on all tracks shall be as follows:

- Unused encoded cards: $0,64 U_R \leq U_i \leq 1,36 U_R$
- Returned cards: $0,52 U_R \leq U_i \leq 1,36 U_R$

NOTE The requirements above specify the interchange signal amplitude limits for each of the encoded track locations at the specified bit densities. Signal amplitude requirements specified in Table 1 reflect the magnetic media limits at the specified recording frequency and recording test currents.

9.5 Bit configuration

Data shall be recorded with the least significant bit (2^0) first and the parity bit last.

9.6 Direction of recording



The encoding shall begin from the right-hand side viewed from the side with the magnetic stripe and with the stripe at the top.

9.7 Leading and trailing clock bits

The lead-in up to the first FSC shall be recorded with ones and the space after the last FSC shall also be recorded with ones. Ones encoded from the edge of the card to 3,30 mm (0.130 in) in from the edge of the card are not required to meet the specifications given herein.

10 Data structure

User data to be written on the card shall be divided among the tracks used. Each track used shall be of a fixed length depending on the type of card used requiring the user data to be padded with binary zero bytes if it does not fill the the available space on the track(s) used. Data structure on each track is independent of other tracks. The general process for structuring of the data is shown in Table 3, see also Figure 11.

Table 3 — Data structure process steps

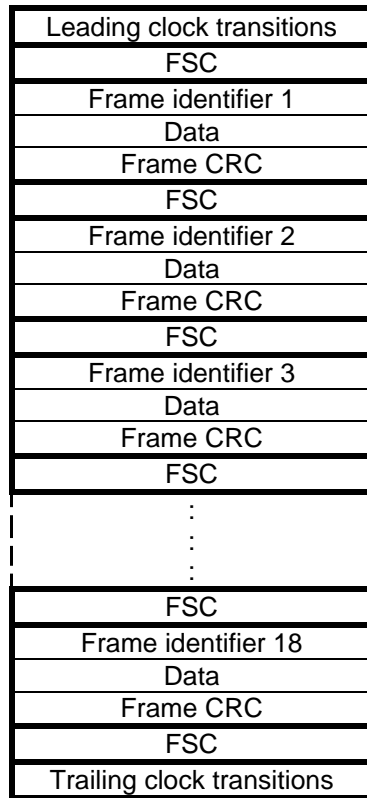
Step	Process
1	Determine number of tracks needed based on data capacity for card type used
2	Divide card data into data for each track and pad end of card data with binary zero bytes if necessary such that all frames on every track used are filled. Padding may be done before dividing onto tracks or after. More error correction is obtained if data is divided equally among the tracks used and then each track padded.
3	Generate CRC for track data and append to end of track data
4	Divide track data into frames
5	Generate Reed-Solomon column parity
6	Add frame ID number
7	Generate CRC for each frame
8	Arrange for writing on card: add lead/trail clock transitions, convert frame ID from 8 bit to 5 bit string, and add FSC's

NOTE No coded character set is defined by this standard. Most uses for the high density format will be for non-text applications.

10.1 Track format

10.1.1 Track layout

Each track shall consist of leading clock transitions, a FSC, data frames each separated by a FSC, a FSC, and trailing clock transitions as shown below.



10.1.2 FSC

The FSC is used to determine direction and identify the edge of the data frame. A FSC shall occur before and after each data frame but only one FSC shall occur between adjacent data frames. The FSC has the representation shown in Figure 10.

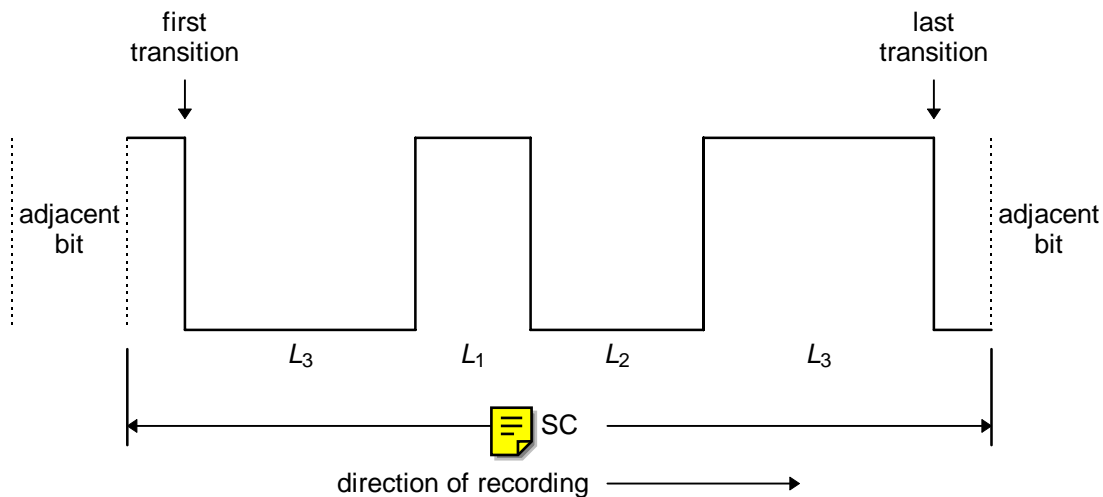


Figure 10 — FSC representation

NOTE The FSC occupies a space of 7,5 bits and is a series of flux transition intervals where the edges are like 1 bits (transition in middle of bit boundary). It is not a series of 1 and 0 bits since this pattern of flux transition intervals is unique and will never occur with MFM encoding rules. Therefore, the FSC can be found even if character synchronisation within the data frame has been lost, and synchronisation begins with the next data frame.

10.1.3 Data frame

Data frames shall consist of a frame number, data and a frame CRC character. The number of frames per track shall be 18.

10.1.3.1 Frame identifier

Each data frame shall be identified with a 5 bit character representing a number. Frames shall be numbered consecutively starting with 1 for the frame nearest the start of encoding and ending with 18 nearest the end of encoding. When performing data operations prior to writing on the card or during the decoding process after reading, the frame identifier shall be represented as an 8 bit string.

10.1.3.2 Data

Data shall be represented in 8 bit bytes and shall be user data, column parity, or track CRC information. The capacity and number of bytes per frame depends on the card size and shall be as shown in Table 4. For each card size all the amounts are fixed (no variable lengths).

Table 4 — Track capacity 

Card type	ID-1	ID-2	ID-3
Data capacity (bytes per frame)	17	22	28
Data capacity (bytes per track)	306	396	504
Column parity (bytes)	68	88	112
Track CRC (bytes)	4	4	4
User data capacity (bytes)	234	304	388

NOTE The user data capacity shown in Table 4 is based on the amount of error correction used.

10.1.3.3 Frame CRC character

Each frame shall include one 8 bit CRC character.

10.2 Coding for error detection and correction

Track data shall be located in frames as shown in Figure 11 where N is 2 plus the number of bytes per track as specified in Table 4. When written on the card, byte 1 of frame 1 is nearest the start of encoding and byte N of frame 18 is nearest the end of encoding (left to right, top to bottom).

Byte										
1	2	3	4		N-5	N-4	N-3	N-2	N-1	N
Frame ID	Data area									CRC
1	CP	CP	CP		CP	CP	CP	CP	CP	CRC ₁
2	CP	CP	CP		CP	CP	CP	CP	CP	CRC ₂
3	CP	CP	CP		CP	CP	CP	CP	CP	CRC ₃
4	CP	CP	CP		CP	CP	CP	CP	CP	CRC ₄
5	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
6	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
7	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
8	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
9	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
10	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
11	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
12	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
13	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
14	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
15	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
16	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
17	Data	Data	Data		Data	Data	Data	Data	Data	CRC ₅
18	Data	Data	Data		Data	Track CRC			CRC ₁₈	

Figure 11 — Track data structure

NOTE The frame CRC character is used for error detection and column parity is used for error correction.

10.2.1 Track CRC

The track shall include a CRC comprised of four 8 bit bytes generated as follows and added to the end of the track data. Highest order term for track CRC shall be in byte (N-4) frame 18.

$$CRC = [x^4 M(x)] \text{ mod } g(x) \text{ over } GF(2^8)$$

where:

M(x) = all user data for the track in polynomial form (length depends on card type used). Highest order term is in byte 2 frame 5 and lowest order term in byte N-5 frame 18. See Figure 11.

g(x) = (x-α)(x-α²)(x-α³)(x-α⁴) = generator polynomial for the track CRC

GF(2⁸) = a finite Galois field of 256 different 8 bit binary symbols generated by a primitive polynomial

$$p(x) = 1 + x^2 + x^3 + x^4 + x^8$$

xⁱ = bit at position i

NOTE 1 The track CRC will allow the detection up to 4 errors with a probability of 1. For more than 4 errors the probability of detecting errors is equal to 1-1/(256)⁴.

NOTE 2 The track CRC is the remainder of x⁴M(x) divided by g(x) using modulo operations. For more than 251 bytes of data, the CRC is technically not a cyclical check, it is a simple redundancy check although it is generated in the same way.

NOTE 3 Published tables exist showing the bit representations for the corresponding power for α for GF power of alpha corresponds to the number so that the term alpha raised to it's power is a unique bit pattern (8bit binary string). Each byte of the user data needs to be converted from 8 bit binary into the corresponding power of α before carrying out the modulo operation. The term x is used to denote the position of the bit in the string for example, x^2 in the track data would be the third lowest order byte.

\$Check the text and notes

10.2.2 Column parity

Column parity shall be generated for each column of byte information across all user data frames on the track using a shortened Reed-Solomon code RS(255-237,251-237), also called RS(18,14). Highest order term for track CP shall be in frame 4 and lowest order term shall be in frame 1.

$$CP = [x^4 M(x)] \bmod g(x) \text{ over } GF(2^8)$$

where:

$M(x)$ = user data for a column from frame 5 to frame 18 in polynomial form (length is fixed at 14). Highest order term is in frame 18 and lowest order term in frame 5. See Figure 11.

$g(x) = (x-\alpha)(x-\alpha^2)(x-\alpha^3)(x-\alpha^4) =$ generator polynomial for the Reed-Solomon CP

$GF(2^8) =$ a finite Galois field of 256 different 8 bit binary symbols generated by a primitive polynomial

$$p(x) = 1 + x^2 + x^3 + x^4 + x^8$$

$x^i =$ bit at position i

NOTE 1 The CP will allow the correction up to 4 "erasures" with a probability of 1. More than 4 "erasures" on a track cannot be corrected. The term "erasure" is used in RS codes and denotes an area is not readable, the decoding process then treats that sector as "erased" or not there.

NOTE 2 The CP is the remainder of $x^4 M(x)$ divided by $g(x)$ using modulo operations. The term "shortened" before Reed-Solomon means that there are higher order terms with a value of 0 (237 in this case) which do not need to be considered in modulo operations.

NOTE 3 This code results in $100\% \cdot 4/18 = 22.2\%$ error correction overhead.

10.2.3 Frame CRC

The frame shall include a CRC comprised of one 8 bit byte generated as follows and added to the end of the frame data.

$$CRC = [xM(x)] \bmod g(x) \text{ over } GF(2^8)$$

where:

$M(x) =$ user data for frame and frame ID number from byte 1 to byte N-1 in polynomial form (length depends on card type used). Highest order term is in byte 1 and lowest order term in byte N-1. See Figure 11

$g(x) = (x-\alpha) =$ generator polynomial for frame CRC

$GF(2^8) =$ a finite Galois field of 256 different 8 bit binary symbols generated by a primitive polynomial

$$p(x) = 1 + x^2 + x^3 + x^4 + x^8$$

$x^i =$ bit at position i

NOTE 1 The frame CRC will allow the detection up to 1 error with a probability of 1. For more than 1 error the probability of detecting errors is equal to $1-1/256$.

NOTE 2 The CRC is the remainder of $xM(x)$ divided by $g(x)$ using modulo operations.

NOTE 3 The frame ID number is an 8 bit byte for all calculations but is written on the card as a 5 bit sequence.

11 Decoding

The general steps to follow for decoding data for each track after reading are shown in Table 5. Specific implementations are left to the user.

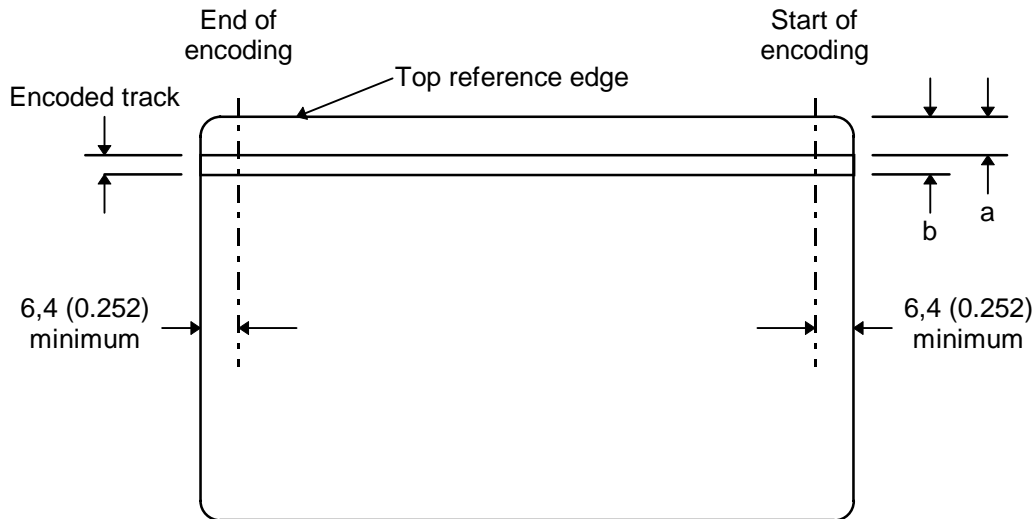
Table 5 — Decoding process

Step	Process
1	Read track data from card.
2	Expand frame ID to 8 bit size.
3	Create data structure as shown in Figure 11.
4	Check track CRC (result of $[x^4M(x) + CRC] \bmod g(x)$ over $GF(2^8)$ must be all 0 bytes for no errors)
	If errors then proceed to following steps
5	Check frame CRC values and identify areas with errors ("erasure" areas).
6	Fill "erased" areas with binary 0 bytes.
7	Decode using Reed-Solomon code RS(18,14) to correct errors.
8	Create new data structure based on correction information.
9	Check track CRC (result of $[x^4M(x) + CRC] \bmod g(x)$ over $GF(2^8)$ must be all 0 bytes for no errors)
	If errors then track is not readable.

NOTE Numerous references are available describing specific decoding implementations. See Annex E.

12 Location of encoded tracks

Each encoded track shall be located between the two lines as shown in Figure 12. The start of encoding is located at the centreline of the first transition of the first FSC. The end of encoding is located at the centreline of the last transition of the last FSC.



dimensions in millimetres (inches)

Track	dimension		
Track H1	a	5,75(0.226)	minimum
	b	6,75(0.266)	maximum
Track H2	a	7,45(0.293)	minimum
	b	8,45(0.333)	maximum
Track H3	a	9,15(0.360)	minimum
	b	10,15(0.400)	maximum
Track H4	a	10,85(0.427)	minimum
	b	11,85(0.467)	maximum
Track H5	a	12,55(0.494)	minimum
	b	13,55(0.534)	maximum
Track H6	a	14,25(0.561)	minimum
	b	15,25(0.601)	maximum

Figure 12 — Location of encoded tracks

Annex A (informative)

Read compatibility of magnetic stripes (ISO/IEC 7811-6 and ISO/IEC 7811-7)

The purpose of this annex is to explain to users the limitations of the term 'read compatibility' as mentioned in the scope of this standard, and applied to ISO/IEC 7811-6 and ISO/IEC 7811-7.

The high density tracks specified in ISO/IEC 7811-7 are arranged such that a reader designed to read these tracks will also be able to read the normal tracks defined in ISO/IEC 7811-6. This also allows a combination of normal and high density tracks to coexist on the same magnetic stripe, for example normal tracks 1 and 2 with high density tracks H5 and H6.

The increased density specified in ISO/IEC 7811-7 will result in a lower signal amplitude compared to ISO/IEC 7811-6: approximately 40% depending on testing of PTB. The exact ratio between these two signal amplitude values will depend on the type of magnetic stripes used.



Annex B (normative)

Signal amplitude measurements

This test method in this annex will be superseded by the next edition of ISO/IEC 10373-2.

Table B.1 shows revised bandpass filter characteristics that shall be used for signal amplitude measurements performed in ISO/IEC 10373-2: 1998.

Table B.1 — Revised values for bandpass filter

Position	cycles per mm (cycles per inch)	flux transitions per mm (flux transitions per inch)
1	0,009 (0.23)	0,018 (0.46)
2	0,025 (0.63)	0,05 (1.27)
3	28 (711)	56,0 (1422)
4	80,0 (2032)	160,0 (4064)

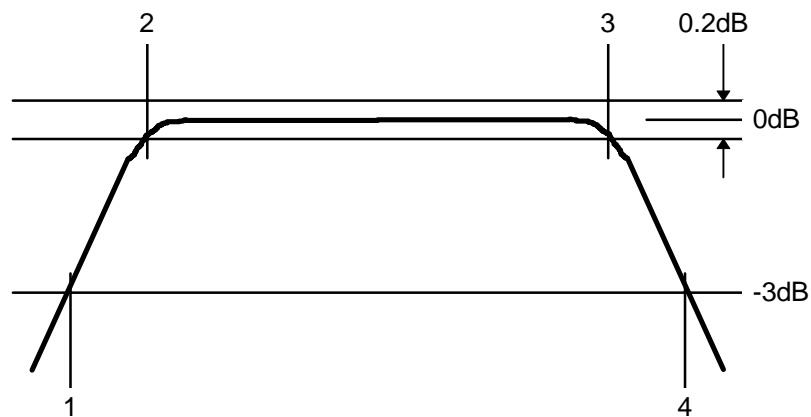


Figure B.1 — Bandpass filter

In addition to ISO/IEC 10373-2 test head specifications, the following apply. \$Note: all values need verification



Table B.2 — Test head specifications

Read gap	6 μm (236 μin)
Read track width	0,6 mm (0.024 in)
Write gap	25,4 μm (1000 μin)
Write track width	1,2 mm (0.047 in)
Width of read or write test head in contact with magnetic stripe	3,2 \pm 0,3 mm

Annex C (informative)

Magnetic stripe abrasivity

The purpose of this annex is to explain why the abrasive properties of magnetic stripes as it relates to head life are not among the physical characteristics governed by this standard. The absence of any specification for abrasive properties reflects the difficulty of defining the parameters of abrasive wear and devising an accurate, repeatable test for measuring abrasive properties. Although no repeatable test methods are available, there are known technologies available for extending head life such as improved head materials, magnetic stripe formulation additives, or overcoats on magnetic stripe.

A quantified stripe abrasivity would seem to be an essential prerequisite to any attempt to predict magnetic head lifetimes. However, just as there is considerable variation in the abrasive nature of different magnetic stripes, there are a multitude of magnetic stripe reader/writer environments. The variety of combinations of influences and the complexity of the manner in which these affect abrasivity makes it extremely difficult to predict magnetic head lifetimes even when the environmental, mechanical and magnetic stripe conditions are specified.

Current equipment-specific abrasivity testing is done on a purely comparative basis. It is time consuming and usually expensive in terms of the number of cards used. The results of such tests are simply rankings that show one stripe to be some degree more or less abrasive than others under the specific conditions of test. There are no accurate absolute values and the rankings may change from one set of conditions to another.

Performing a successful read or write operation on a magnetic stripe requires the stripe and magnetic head to be in contact for the whole operation. The relative movement between the magnetic head and magnetic stripe produces wear of both. Initially the abrasivity of the magnetic stripe falls rapidly with the number of head passes, so that the abrasivity of a new unused magnetic stripe may be much greater than that of a magnetic stripe which has only been written once, but as the number of head passes increases the rate of change of abrasivity decreases.

The influences affecting magnetic stripe abrasivity are known to include temperature, humidity, head material (and its state of wear and finish), head pressure, card speed, the specific physical properties of the magnetic stripe surface in contact

with the head, surface roughness, and contamination on the magnetic stripe. Under field conditions dust, dirt and grease from the environment are deposited at the head/stripe interface often producing major discrepancies between abrasive wear measured under laboratory conditions and that actually achieved.

It may be seen, therefore, that not only are there difficulties involved in achieving an acceptable level of measurement uncertainty for abrasivity testing but that there are significant doubts regarding the applicability of the results of abrasivity tests on cards under laboratory conditions to predictions of performance in the real world. Unless these problems are resolved, there can be no useful standard specification and test.

Annex D (informative)

Static magnetic characteristics

D.1 Introduction

This annex gives definitions and values for certain static magnetic characteristics of high coercivity magnetic stripes. These parameters are useful in the manufacture of magnetic material and are not directly related to magnetic performance characteristics given in Table 1 for cards. There is no guarantee that magnetic stripes with values given in this informative annex will meet the mandatory requirements given in Table 1. However, magnetic stripes that do not comply with the suggested static magnetic values probably will not conform to Table 1 properties.

D.2 Definitions

D.2.1

maximum field, H_{\max}

maximum absolute magnetic field strength applied as described by the test method

D.2.2

static M(H) loop

normal hysteresis loop for which the magnetic field strength is cycled between the extremes - H_{\max} to + H_{\max} at such a low rate of change that the loop is not influenced by the rate of change (see IEC 50(221))

D.2.3

coercivity, $H'_{cM} = H'_{cJ}$

continuously applied magnetic field which reduces the magnetisation to zero from a previous maximum magnetisation state in the opposite direction, the quantity of interest being that which is measured parallel to the longitudinal axis of the stripe (see IEC 50(221))

D.2.4

remanence, M_r

value of magnetisation (M) in a given direction at zero magnetic field ($H=0$) after the application and removal of the maximum field (H_{\max}) in the same direction

D.2.5

remanence coercivity, H_r

applied magnetic field which when removed returns the material to a zero magnetisation state from a previous maximum magnetisation state in the opposite direction, the quantity of interest being that which is measured parallel to the longitudinal axis of the stripe

D.2.6

oersted, Oe

Gaussian cgs unit of magnetic field strength which is commonly used in the magnetic recording industry equal to approximately 79,578 A/m (relationship is informative, see annex A (informative) of ISO 31-5:1992)

D.2.7

static demagnetisation, S_{160}

reduction in remanent magnetisation under the influence of an opposing magnetic field; characterised by $(M_r - M^+(-160))/M_r$

D.2.8**longitudinal squareness, $SQ = M_r/M$ at (H_{max})**

ratio of the value of remanence (M_r) after the application and removal of the maximum field (H_{max}) to the magnetisation (M) at the maximum field applied (H_{max}) measured along the longitudinal axis of the stripe

D.2.9**remanence ratio, $R_M = (M_{rP} / M_{rL})$**

ratio of the perpendicular remanence measured perpendicular to the surface of the magnetic stripe (M_{rP}) to the longitudinal remanence measured along the longitudinal direction of the magnetic stripe (M_{rL})

D.2.10**switching field by slope, SF_S**

$(|H_2| - |H_1|) / H'_{cM}$ where $M(-|H_1|) = 0,5M_r$ and $M(-|H_2|) = -0,5 M_r$; the difference between the field values at the intercept of the static magnetisation $M(H)$ loop with $M(H) = 0,5 M_r$ and $M(H) = -0,5 M_r$, divided by the coercivity

D.2.11**switching field by derivative, SF_D**

the width at half height of the differentiated static magnetisation curve $M(H)$ divided by the coercivity value on the same curve

D.3 Recommended characteristics

The recommended static characteristics of high coercivity magnetic stripe are shown in Table D.1

Table D.1 — Static characteristics of high coercivity magnetic material

Number	Parameter	Symbol	Value
1	Coercivity	H'_{cM}	250 kA/m (3125 Oe) maximum 200 kA/m (2500 Oe) minimum
2	Static demagnetisation	S_{160}	0,20 maximum
3	Longitudinal squareness	SQ	0,80 minimum
4	Remanence ratio	R_M	0,35 maximum
5	Switching field by slope	SF_S	0,30 maximum
6	Switching field by derivative	SF_D	0,50 maximum

NOTE Table D.1 is identical to that given in ISO/IEC 7811-2 and ISO/IEC 7811-6 except for the maximum recommended coercivity.

Annex E (informative)

Reed-Solomon code references

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